

<b>Notice of References Cited</b>	Application/Control No. 10/707,388	Applicant(s)/Patent Under Reexamination DIVAKARUNI ET AL.	
	Examiner Steven J. Fulk	Art Unit 2891	Page 1 of 1

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	B	US-2003/0209764	11-2003	Ishikura et al.	257/382
	C	US-6,069,398	05-2000	Kadosh et al.	257/538
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, "Silicon Processing for the VLSI Era," 1986, Volume I, pages 387,400.
	V	Wolf, "Silicon Processing for the VLSI Era," 1990, Volume II, page 146.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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